

### Subject Description Form

<b>Subject Code</b>	EIE2100 (for 42470)
<b>Subject Title</b>	Basic Circuit Analysis
<b>Credit Value</b>	3
<b>Level</b>	2
<b>Pre-requisite/ Co-requisite/ Exclusion</b>	Nil
<b>Objectives</b>	<ol style="list-style-type: none"> <li>1. Introduce fundamental circuit theory.</li> <li>2. Develop ability for solving problems involving electric circuits.</li> <li>3. Develop skills for experimentation on electric circuits.</li> <li>4. Impart relevant skills and knowledge for independent learning of other subjects that require such skills and knowledge.</li> </ol>
<b>Intended Subject Learning Outcomes</b>	<p><b>Upon completion of the subject, students will be able to:</b></p> <p><u>Category A: Professional/academic knowledge and skills</u></p> <ol style="list-style-type: none"> <li>1. Acquire a good understanding of fundamental circuit theory.</li> <li>2. Solve simple problems in electric circuits.</li> <li>3. Use suitable instrumentation to carry out experimental investigations to validate the theoretical investigations.</li> </ol> <p><u>Category B: Attributes for all-roundedness</u></p> <ol style="list-style-type: none"> <li>4. Search for useful information in solving problems in electric circuits.</li> </ol>
<b>Subject Synopsis/ Indicative Syllabus</b>	<p><b>Syllabus:</b></p> <ol style="list-style-type: none"> <li>1. <u>Basic Concepts of Electricity</u> Static electricity, Conductors, insulators and electron flows. Concept of electric circuits. Voltage, current, and resistances in a practical circuit.</li> <li>2. <u>DC Circuits</u> Introduction to electric circuits. Kirchhoff's current and voltage laws. Independent and dependent sources. Simple circuits: voltage divider, current divider, series and parallel circuits. Trees &amp; Co-trees. Cutsets &amp; loops. Nodal and mesh analyses. Loop and cutset analyses of resistive circuits. Thévenin and Norton theorems. Power dissipation. Source loading and maximum power transfer.</li> <li>3. <u>Capacitance, Inductance and First Order Transient</u> Constitutive relations of capacitor and inductor. Introduction to time-varying circuits. Simple RC and LC circuits. Important concept of independent state variables. First-order differential equation (with simple solution of exponential form). First order transient analysis. Time-domain solution and transient behaviour of first order circuits.</li> <li>4. <u>Introduction to Transformers</u> Concept of ideal transformer (assuming sinusoidal voltages and currents). Dot convention. Physical transformer as ideal transformer with leakage and magnetizing inductances. Applications in galvanic isolation and voltage/current level conversion.</li> <li>5. <u>Steady-state Analysis of AC Circuits</u> Average and rms values. Phasors (rotating vectors). Steady-state analysis of circuits driven by single fixed frequency sinusoidal sources. Impedance and admittance. Euler equation. Analysis approach 1: phasor diagrams for simple</li> </ol>

circuits. Analysis approach 2: systematic complex number analysis, i.e., same treatment as DC circuits but with complex numbers representing phase and magnitude of AC voltages and currents. Real and reactive powers. Power factor. Simple three-phase circuits.

6. Operational Amplifiers  
 Ideal operational amplifier. Defining characteristics (i.e., infinite gain and infinite input resistance). Op-amp circuits: inverting amplifier, non-inverting amplifier, summer, difference amplifier, integrator and differentiator. Applications: instrumentation amplifier; current-to-voltage and voltage-to-current converters.

**Laboratory Experiments:**

1. Introduction to laboratory instrumentation / Thévenin and Norton theorems
2. First order transient
3. Use of operational amplifiers.

Teaching/ Learning Methodology	Teaching and Learning Method	Intended Subject Learning Outcome	Remarks
	Lectures, supplemented with interactive questions and answers	1, 2, 4	In lectures, students are introduced to the <i>knowledge</i> of the subject, and <i>comprehension</i> is strengthened with interactive Q&A.
	Tutorials, where problems are discussed and are given to students for them to solve	1, 2, 4	In tutorials, students <i>apply</i> what they have learnt in solving the problems given by the lecturer.
	Laboratory sessions, where students will perform experimental verifications. They will have to record results and write a report on one of the experiments.	2, 3, 4	Students <i>acquire</i> hands-on experience in using electronic equipment and <i>apply</i> what they have learnt in lectures/tutorials to experimentally validate the theoretical investigations.
	Assignments	1, 2, 3, 4	Through quizzes and working assignments, students will develop a firm understanding and <i>comprehension</i> of the <i>knowledge</i> taught.

**Alignment of Assessment and Intended Learning Outcomes**

Specific Assessment Methods/ Task	% Weighting	Intended Subject Learning Outcomes to be Assessed (Please tick as appropriate)			
		1	2	3	4
1. Continuous Assessment (Total 40%)					
• Assignments	10%	✓	✓		✓
• Laboratory works and reports	10%		✓	✓	✓
• Mid-semester test	10%	✓	✓		✓
• End-of-semester test	10%	✓	✓		✓
2. Examination	60%	✓	✓		✓
Total	100%				

**Explanation of the appropriateness of the assessment methods in assessing the intended learning outcomes:**

Specific Assessment Methods/Tasks	Remark
Assignments	Assignments are given to students to assess their competence level of <i>knowledge</i> and <i>comprehension</i> . These assignments include the quizzes made during the lectures and take-home exercises. The criteria (i.e. <i>what</i> to be demonstrated) and level (i.e. the <i>extent</i> ) of achievement will be graded according to five levels: Excellent (A+, A and A-), Good (B+, B and B-), Satisfactory (C+, C and C-), Pass (D+ and D) and Fail (F). These will be made known to the students before an assignment is given. Feedback about their performance will be given promptly to students to help them improvement their learning.
Laboratory works and reports	Students will be required to perform three experiments and submit three reports on the experiments made. Students also need to do three demonstrations on one of the questions in each experiment. Expectation and grading criteria will be given as in the case of assignments.
Mid-semester test	There will be a mid-semester test to evaluate students' achievement of all the learning outcomes and give feedback to them for prompt improvement. Expectation and grading criteria will be given as in the case of assignments.
End-of-semester test and Examination	There will be an end-of-semester test and examination to assess students' achievement of all the learning outcomes. These are mainly summative in nature. Expectation and grading criteria will be given as in the case of assignments.

<b>Student Study Effort Expected</b>	<b>Class contact (time-tabled):</b>	
	• Lecture	24 Hours
	• Tutorial/Laboratory/Practice Classes	15 hours
	<b>Other student study effort:</b>	
	• Lecture: preview/review of notes; homework/assignment; preparation for test/quizzes/examination	36 Hours
	• Tutorial/Laboratory/Practice Classes: preview of materials, revision and/or reports writing	30 Hours
	<b>Total student study effort:</b>	<b>105 Hours</b>
<b>Reading List and References</b>	<p><b>Textbook:</b></p> <ol style="list-style-type: none"> <li>1. W.H. Hayt, J.E. Kemmerly and S.M. Durbin, <i>Engineering Circuit Analysis</i>, 7<sup>th</sup> ed., New York: McGraw-Hill, 2006.</li> <li>2. G. Rizzoni, <i>Fundamentals of Electrical Engineering</i>, 1<sup>st</sup> ed., McGraw-Hill, 2009.</li> </ol> <p><b>References:</b></p> <ol style="list-style-type: none"> <li>1. C.K. Tse, <i>Linear Circuit Analysis</i>, London: Addison-Wesley, 1998.</li> <li>2. D.A. Neamen, <i>Micoelectronics: Circuit Analysis and Design</i>, Boston: McGraw-Hill, 3<sup>rd</sup> ed., 2007.</li> <li>3. R.A. DeCarlo and P.M. Lin, <i>Linear Circuit Analysis</i>, 2<sup>nd</sup> ed., Oxford University Press, 2001.</li> <li>4. A.H. Robbins and W.C. Miller, <i>Circuit Analysis: Theory and Practice</i>, Thomson Learning, 4<sup>th</sup> ed., 2006.</li> </ol>	
<b>Last Updated</b>	March 2021	
<b>Prepared by</b>	Dr Daniel Lun	